

MATLAB SIMULINK Model for Flyback Inverter with Active Clamp Technique

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Abstract: The interleaved flyback inverter is the proposed system.. Two flyback inverters are connected and the control strategy used is the active clamp technique. This technique reduces the voltage spikes across the main switch during resonance, and the switching losses are reduced along with voltage stress. The system is modified as an energy storage application. The efficiency is improved and The simulations are done using MATLAB .The explanations theories and results are discussed.

Keywords: ILFI, induction motor drive, active clamp, energy storage.

I. **INTRODUCTION**

The drive system plays an important role in the modern This process of energy storage has been implemented industries. The most significant drive system in industries using devices which serve as an accumulator. In this paper are the induction motor drive system. Based on their interleaved flyback inverter has been modified for energy application and requirements they are classified into single storage application. The interleaved flyback inverter is phase and three phase AC induction motor. Washing machine, furnace, conveyor, pump, wind tunnels are the areas where they are used.speed variations, heavy load inertial starting with higher starting torque are the significant applications of induction motor. Desired output at maximum efficiency with low losses are produced by induction motors.Voltage these variations, unbalance, motor load, THD and power factor are the factors affecting motor efficiency.this paper deals with ILFI[1].(interleaved flyback inverter) fed induction motor with space vector modulation technique.. and active clamp technique is implemented[2]. The clamp circuit is used to reduce the switching loss and the voltage spikes across the main switch.

Operation at increased power levels are obtained by using interleaving concept. The RMS current in the input capacitor is reduced. this results in the reduced number of capacitors and less expensive. primary and secondary transformer winding peak currents are reduced.the transientresponse is improved and the electromagnetic interference is improved as a result of interleaving concept.The decoupling capacitor .first phase converter, second phase converter, unfolding bridge, C-L filter are the components of the ILFI converter.a 120 Hz harmonic frequency is removed by using the decoupling capacitor. The main switches are present in the each phase of the flyback converteralong with clamp citcuit, transformer and diodes. The voltage across the main switch is reduced by the clamp circuit. The configuration and structure of the interleaved flyback inverter is described below along with it's modes of operation. The Energy storage has become an important application nowadays, The energy storage is an old process such that energy during the creation of the universe has been stored in sun, moon etc but now humans use them directly through solar and by changing them into electricity.

simulated with induction motor drive and the system is simulated for energy storage application using MATLAB. The calculations are presented for switching loss, voltage stress and efficiency is improved.

ILFI CONFIGURATION

II.

The interleaved flyback inverter has two flyback inverters connected parallel and the technique used is the active clamp technique. This technique is used to reduce the switching loss of the switches.





the isolation between the PV module and the grid line is provided by the isolation transformer.boosting of voltage also takes place.the connection between the AC power provided through transformer grid line is employed by unfolding bridge.

MODES OF OPERATION III.

The single phase converter without the active clamp circuit is activated by the interleaved flyback inverter using the phase control method and the active clamp control method.





MODE1

All the other switches are turned off except the switch Sp1.this energizes the leakage and magnetic inductance.the current flow is through the magnetic flux,this forward biases diode D1. the energy is delivered to the load through the output capacitor

MODE2

In this mode the energization of leakage and magnetic inductance takes place with Sp1 and Sa1 turned on the energy is transferred to the load through the output capacitor.

MODE3

In this mode both the phases are turned on.Sp1,Sp2,Sa1,Sa2 are turned on.the capacitor delivers energy to the load.

IV. ACTIVE CLAMP CONTROL METHOD

Solar irradiance and atmospheric temperature are the two parameters influencing the output power of the PV module .the ILFI efficiency has to be improved which depends on the irradiance values of the weather conditions .the active clamp circuit comprises of the switch Sa1 and a capacitor in each phase of the converter.this helps in reducing the voltage spikes across the main switch.thus when each phase of the ILFI is controlled the the losses are minimized.when Sp1 is turned off the voltage is formed by Sp1 votage without clamp circuit sum of input voltage Vin through PV module feedback voltage,spike voltage Vsp1.



Fig 3 energy storage

The energy storage consists of the interleaved flyback inverter with three switches which act as the main switch and there are two DC supplies as the input source when one of the source is assigned a value the other is turned off and when the other switch has input voltage this switch switch which was turned on before will be turned off.

VI. SIMULATION RESULTS



Fig 7 simulink model of ILFI R load

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Fig 8 output voltage and output current



Fig 9 simulink model of ILFI motor load



Fig 10 output voltage



Fig 11 output current Copyright to IJIREEICE





Fig 12 speed and torque curve



Fig 13 energy storage simulink model





Fig 14 output voltage





Fig 16 simulink model with motor load



Fig 17 output voltage



Fig 18 output current



Fig 19 speed torque curve

Fig 22 hardware proptotype with solar panel CONCLUSION VIII.

VII. CALCULATION

CONVENTIONAL SYSTEM Stress :S=F/Vo =50/70 =0.714S=0.714 Efficiency E=2(1-D)*V;V=output-input =70-30 =40 E=2(1-0.5)*40 E=40% PROPOSED SYSTEM Stress: s= F/Vo =50/110 = 0.45 Efficiency E=2(1-D)*V V=output-input =110-20=90 E=2(1-0.5)*90 E=90% SWITCHING LOSS =C*Vds*Fsw =400*10^-12*55*70000 = 0.00154 HARDWARE RESULTS Fig 20. Output voltage



4 Fig 21 inverter pulse



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Thus the switching losses and the voltage stress are reduced and the efficiency is improved compared to the existing system. The simulations are done using MATLAB and the results are presented The system has been modified for energy storage application and simulated with induction motor drive as the load.

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